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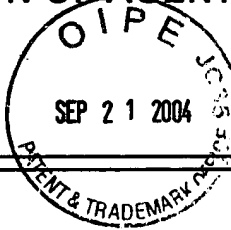


PTO/SB/81 (11/96)  
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Application Number	09/882,786
Filing Date	June-15, 2001
First Named Inventor	William Schmitz
Group Art Unit	2858
Examiner Name	Walter Benson
Attorney Docket Number	1003-0559 / 00-573

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LEO J. PETERS	33,562

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Address	Patent Law Department				
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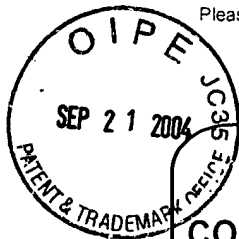
☐ Applicant

☒ Assignee of Record of the entire interest

*Certificate under 37 CFR 3.73(b) is enclosed*

**SIGNATURE of Applicant or Assignee of Record**

Name	SANDEEP JAGGI, Chief IP Counsel and Assistant Corporate Secretary, LSI Logic Corp.
Signature	
Date	9-17-04



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## CHANGE OF CORRESPONDENCE ADDRESS Application

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Application Number	09/882,786
Filing Date	June-15, 2001
First Named Inventor	William Schmitz
Group Art Unit	2858
Examiner Name	Walter Benson
Attorney Docket Number	1003-0559 / 00-573

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SANDEEP JAGGI,  
Chief IP Counsel, Assistant Corporate Secretary, LSI Logic

Signature

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9-17-04

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# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**In re Application of :**

William Schmitz et. al.

**Serial No. :**

09/882,786

**Filed :**

June 15, 2001

**For :**

Testing Implementation For Signal  
Characterization



**Group Art Un**

2858

**Examiner:**

Benson, Walter

**Attv Docket :**

1003-0559 / 00-573

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to:  
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on the date below:

Connie Del Castillo

9/21/04 *Connie del Castillo*  
Date Signature

## **CERTIFICATION UNDER 37 C.F.R. 3.73(b)**

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Sir:

LSI Logic Coporation, a Delaware corporation, certifies that it is the assignee of the entire right, title and interest in the patent application.

The undersigned has reviewed all the documents in the chain of title of the patent application identified above and, to the best of the undersigned's knowledge and belief, title is in the assignee identified above.

The undersigned (whose title is supplied below) is empowered to act on behalf of the assignee.

I hereby declare that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true; and further, that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, inder Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

LSI Logic Corporation  
1621 Barber Lane, MS D-106  
Milipitas, CA 95035  
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Date: 9-17-04

Respectfully submitted,

Sandeep Jaggi  
Chief IP Counsel  
Corporate Assistant Secretary  
LSI LOGIC CORPORATION